

**INFORMATION DISCLOSURE STATEMENT**

Applicant	:	Todd <i>et al.</i>
App. No	:	10/623,482
Filed	:	July 18, 2003
For	:	METHOD TO FORM ULTRA HIGH QUALITY SILICON-CONTAINING COMPOUND LAYERS
Examiner	:	Ron Everett Pompey
Art Unit	:	2812

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application is a PTO/SB/08 Equivalent listing 10 references to be considered by the Examiner. Also enclosed are 3 non-patent literatures as listed on the Information Disclosure Statement.

This Information Disclosure Statement is being filed after the mailing date of a final action or after the mailing date of a Notice of Allowance. This Statement is accompanied by the fee set forth in 37 C.F.R. § 1.17(p). The Commissioner is hereby authorized to charge any additional fees which may be required or to credit any overpayment to Account No. 11-1410.

**CERTIFICATION UNDER 37 C.F.R. § 1.97(e)(2)**

I hereby certify that no item of information contained in this Statement was cited in a communication from a foreign Patent Office in a counterpart foreign application, and, to my

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Docket No. ASMEX.376A  
Customer No. 68,852

knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than 3 months prior to the filing of this Information Disclosure Statement.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 9/27/07

By: S. Wong

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